650V, 8.2A, 268mΩ N-channel Power SuperJunction MOSFET

JMH65R360PF

Features

- $\bullet \;\;$ Excellent $R_{DS(ON)}$ and Low Gate Charge
- 100% UIS Tested
- 100% ΔVds Tested
- Halogen-free; RoHS-compliant

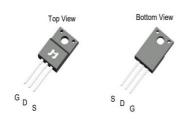
Applications

- SMPS with PFC
- Flyback and LLC topologies
- Silver ATX,adapter,TV,lighting,Telecom

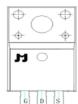
Product Summary

Parameters	Value	Unit
V_{DSS}	650	V
$V_{GS(th)_Typ}$	3.4	V
I _D (@V _{GS} =10V)	8.2	Α
$R_{DS(ON)_Typ}(@V_{GS}=10V$	268	mΩ

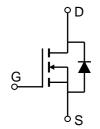








Pin Assignment



Schematic Diagram

Ordering Information

Device	Marking	MSL	Form	Package	Tube(pcs)	Per Carton (pcs)
JMH65R360PF-U	H65R360P	N/A	Tape&Reel	TO-220FP	50	5000

Absolute Maximum Ratings (@ T_C = 25°C unless otherwise specified)

Symbol	Parameter		Value	Unit
V_{DS}	Drain-to-Source Voltage		650	V
V_{GS}	Gate-to-Source Voltage		±30	V
	Continuous Drain Current	$T_C = 25^{\circ}C$	8.2	Λ
I _D		$T_C = 100$ °C	5.2	A
I _{DM}	Pulsed Drain Current (1)		Refer to Fig.4	Α
E _{AS}	Single Pulsed Avalanche Energy (2)		88	mJ
P _D	Power Dissipation	$T_C = 25^{\circ}C$	60	W
		$T_C = 100$ °C	24	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
T_{J}, T_{STG}	Junction & Storage Temperature Range		-55 to 150	°C

Thermal Characteristics

Symbol Parameter		Max	Unit
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient ⁽³⁾	39	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction to Case	2.1	C/VV



Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Cha	racteristics					
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	650	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 650 V, V_{GS} = 0 V$	-	-	10.0	μА
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 30V$	-	-	±100	nA
On Cha	racteristics			•		
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.4	3.4	4.4	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽⁴⁾	$V_{GS} = 10V, I_D = 5.5A$	-	268	348	mΩ
Dynami	ic Characteristics					
R_g	Gate Resistance	f = 1MHz	-	18	-	Ω
C_{iss}	Input Capacitance	., ., ., ., ., .,	382	535	722	pF
C _{oss}	Output Capacitance	$V_{GS} = 0V, V_{DS} = 325V,$ f = 1MHz	19	26	35	pF
C_{rss}	Reverse Transfer Capacitance		-	7.7	-	pF
Q_g	Total Gate Charge	V 0. 40V	12	17	24	nC
Q_{gs}	Gate Source Charge	$V_{GS} = 0 \text{ to } 10V$ $V_{DS} = 325V, I_D = 5.5A$	-	3.6	-	nC
Q_{gd}	Gate Drain("Miller") Charge	VDS = 323 V, ID = 3.5/V	-	7.4	-	nC
		·				
Switchi	ng Characteristics	-		Т	ı	T
t _{d(on)}	Turn-On DelayTime		-	19	-	ns
t _r	Turn-On Rise Time	$V_{GS} = 10V, V_{DD} = 325 V$	-	31	-	ns
$t_{d(off)}$	Turn-Off DelayTime	I_{D} = 5.5A, R_{GEN} = 24 Ω	-	115	-	ns
t _f	Turn-Off Fall Time		-	21	-	ns
Body D	iode Characteristics					
I_S	Maximum Continuous Body Diode Forward Current		-	-	8	Α
I _{SM}	Maximum Pulsed Body Diode Forward Curr	ent	-	-	33	А
V_{SD}	Body Diode Forward Voltage	$V_{GS} = 0V, I_{S} = 5.5A$	-		1.2	V
trr	Body Diode Reverse Recovery Time	I _F = 5.5A, di/dt = 100A/us	192	269	363	ns
Qrr	Body Diode Reverse Recovery Charge	T _F = 5.5A, ui/ut = 100A/us	-	2.9	-	uC

Notes:

 $^{{\}bf 1.}\ Repetitive\ Rating:\ Pulse\ Width\ Limited\ by\ Maximum\ Junction\ Temperature.$

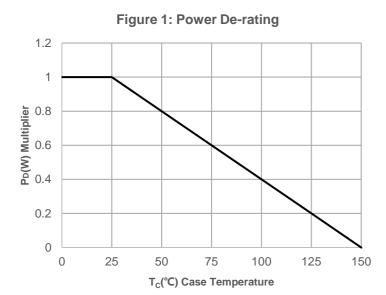
 $^{2.\;}E_{AS}\;condition:\;Starting\;T_J=25C,\;V_{DD}=75V,\;V_{GS}=10V,\;R_G=25ohm,\;L=10mH,\;I_{AS}=4.2A,\;V_{DD}=0V\;during\;time\;in\;avalanche.$

^{3.} $R_{\theta JA}$ is measured with the device mounted on a 1inch² pad of 2oz copper FR4 PCB.

^{4.} Pulse Test: Pulse Width $\!\!\!\!<\!300\mu s,$ Duty Cycle $\!\!\!<\!0.5\%.$



Typical Performance Characteristics



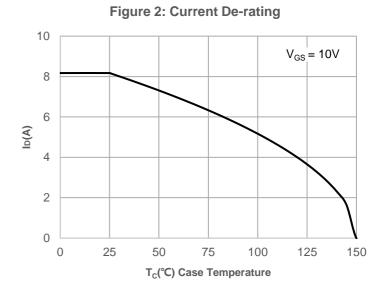
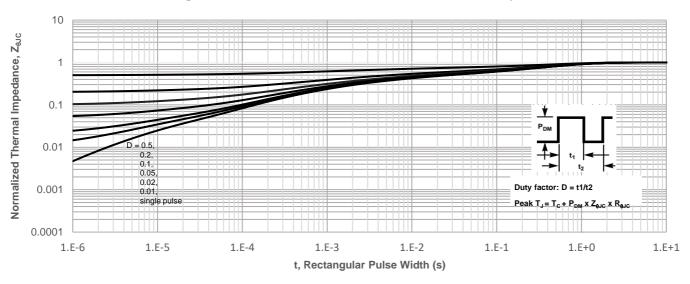
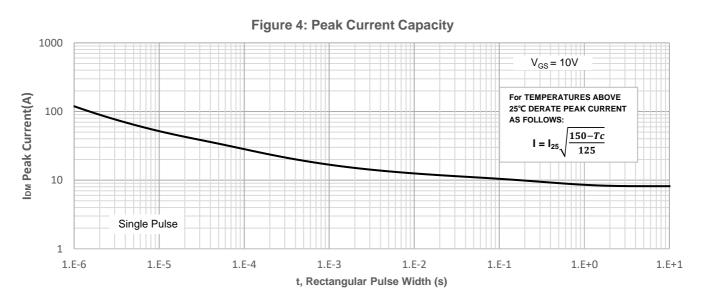


Figure 3: Normalized Maximum Transient Thermal Impedance







Typical Performance Characteristics

Figure 5: Output Characteristics

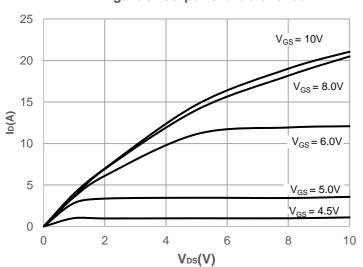


Figure 6: Typical Transfer Characteristics

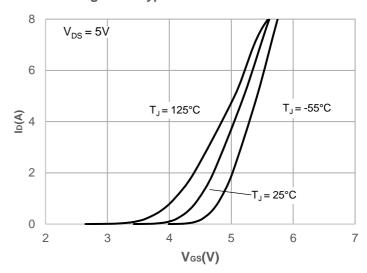


Figure 7: On-resistance vs. Drain Current

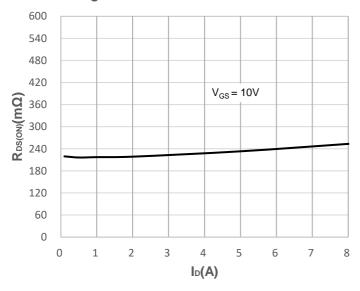


Figure 8: Body Diode Characteristics

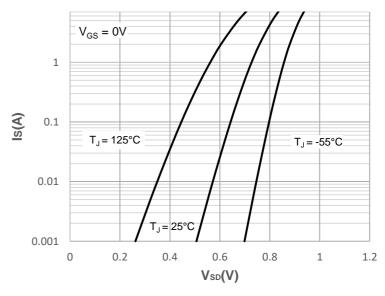


Figure 9: Gate Charge Characteristics

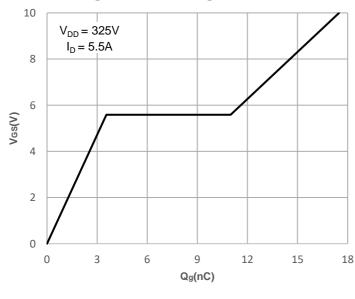
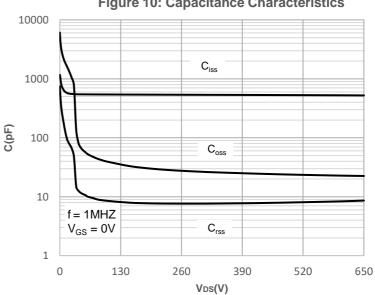


Figure 10: Capacitance Characteristics





Typical Performance Characteristics

Figure 11: Normalized Breakdown voltage vs. Junction Temperature

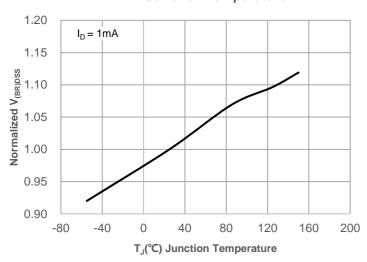


Figure 13: Normalized Threshold Voltage vs. Junction Temperature

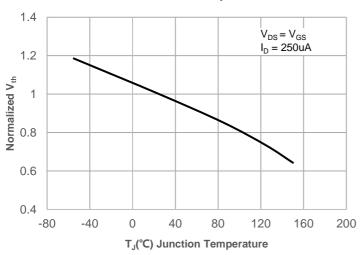


Figure 15: Maximum Safe Operating Area

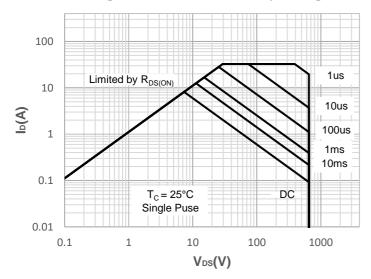
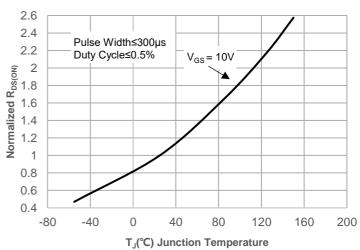
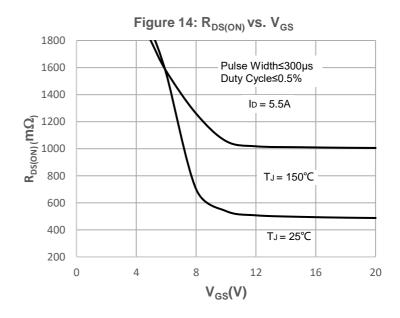


Figure 12: Normalized on Resistance vs. Junction Temperature







Test Circuit

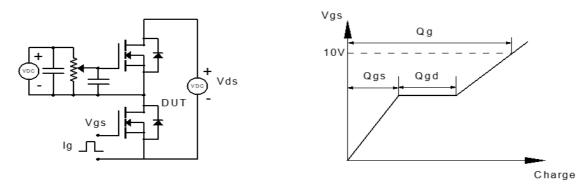


Figure 1: Gate Charge Test Circuit & Waveform

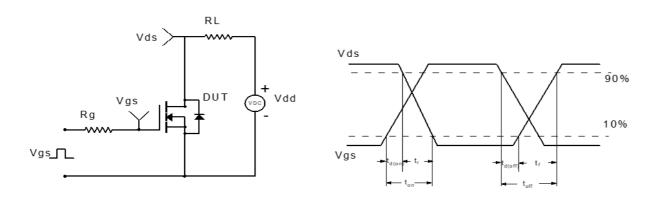


Figure 2: Resistive Switching Test Circuit & Waveform

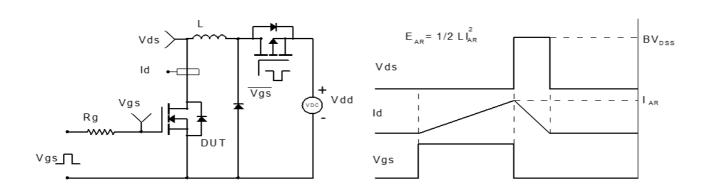


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

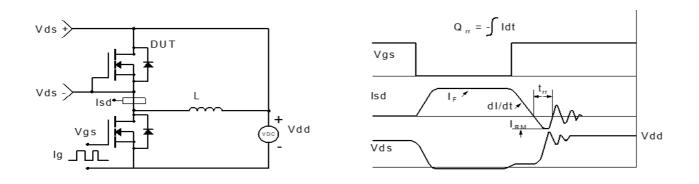
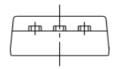


Figure 4: Diode Recovery Test Circuit & Waveform

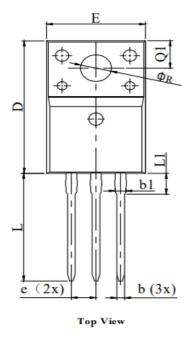


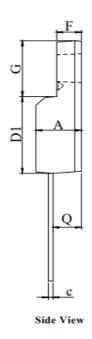
Package Mechanical Data(TO-220F-3L)

Package Outline



Front View





DIM	MILLIMETER				
DIM.	MIN.	NOM.	MAX.		
A	4.50	4.70	4.90		
D	15.20	16.10			
D1	8.80		9.50		
Е	9.70	10.10	10.40		
F	2.44		2.75		
ь	0.70	0.80	0.91		
b1	1.10	1.35	1.55		
с	0.45	0.50	0.65		
e	2.54 BSC				
G	6.40	6.70	6.90		
L	12.00	13.10	14.50		
L1	3.13		3.57		
Q	2.60	2.75	2.85		
Q1	3.20	3.30	3.40		
R	3.05		3.28		

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